

Generation of Equivalent Circuit from Finite Element Model Using Model Order Reduction

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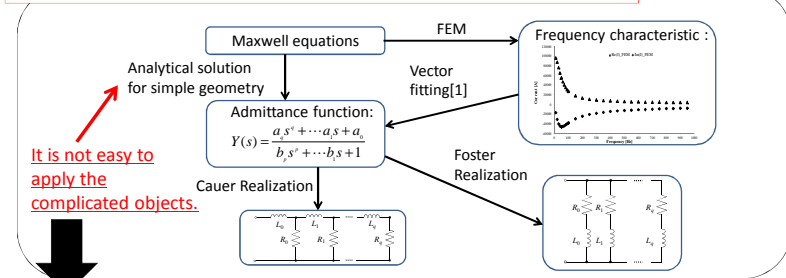
Introduction

Finite element method (FEM) has been widely used to develop and design electromagnetic devices.

We want to develop and design the electromagnetic devices **considering the control and driving circuits** connected to FE model.

The electromagnetic devices are often modeled as an **equivalent circuit** for design of the control and driving circuit.

Equivalent circuits using Rational Polynomial Approximation



Purpose

We propose a novel method to generate the equivalent circuit of the electromagnetic devices using model order reduction.

Padé approximation via the Lanczos process[2]

Laplace-transformed discrete Maxwell equations

$$sNx + Kx = bv$$

$$i = I'x$$

$N, K \in \mathbb{R}^{n \times n}$, $b, I, x \in \mathbb{R}^n$
 v : voltage, i : current

Admittance function

$$Y(s) = I'(K + sN)^{-1}b$$

Spectral decomposition of A

$$s \rightarrow s_0 + \sigma$$

$$Y(s_0 + \sigma) = I'(I - \sigma A)^{-1}r$$

$$A = -(K + s_0 N)^{-1}N$$

$$r = (K + s_0 N)^{-1}b$$

$$Y(s_0 + \sigma) = I'(I - \sigma A S^{-1})^{-1}r = \sum_{i=1}^n \frac{f_i g_i}{1 - \sigma \lambda_i}$$

This formulation would be **unsuitable** for real uses because of heavy computational burden in solution of the eigenvalue problem.

Admittance function

$$Y(s_0 + \sigma) = I'(I - \sigma A)^{-1}r$$

Neumann series expansion

$$Y(s_0 + \sigma) = I'(I + \sigma A + \sigma^2 A^2 + \dots)r = \sum_{i=0}^{\infty} m_i \sigma^i$$

Lanczos method

Reduced admittance function

$$Y_q(s_0 + \sigma) = \sum_{i=0}^q I' r (e_i^T T_q^{-1} e_i) \sigma^i$$

$$= I' r e_i^T (I - \sigma T_q)^{-1} e_i$$

Spectral decomposition of T_q

$$Y_q(s_0 + \sigma) = I' r e_i^T (I - \sigma S_q \Lambda_q S_q^{-1})^{-1} e_i$$

$$= \sum_{j=1}^q \frac{I' r \mu_j v_j}{1 - \sigma \lambda_j}$$

$T_q \in \mathbb{R}^{q \times q}$: Tridiagonal matrix ($n \gg q$)

Lanczos method

We can obtain tridiagonal matrix T_q whose eigenvalues correspond to the significant eigenvalues of A.

-----algorithm-----

- 0) Set $\rho_1 = \|r\|_2$, $\eta_1 = \|I\|_2$, $v_1 = r/\rho_1$, $w_1 = I/\eta_1$, $v_0 = w_0 = 0$ and $\delta_0 = 0$
 For $n=1, 2, \dots, q$ do
- 1) Compute $\delta_n = w_n^T v_n$
- 2) Set $\alpha_n = w_n^T A v_n / \delta_n$, $\beta_n = \eta_n \delta_n / \delta_{n-1}$, $\gamma_n = \rho_n \delta_n / \delta_{n-1}$
- 3) Set $v = [A v_n - \alpha_n v_n - \beta_n v_{n-1}]$, $w = [A^T w_n - \alpha_n w_n - \gamma_n w_{n-1}]$
- 4) Set $\rho_{n+1} = \|v\|_2$, $\eta_{n+1} = \|w\|_2$, $v_{n+1} = v/\rho_{n+1}$, $w_{n+1} = w/\eta_{n+1}$

We need to solve the following equations to obtain $A v_n$ and $A^T w_n$

$$A v_n = -(K + s_0 N)^{-1} N v_n$$

$$A^T w_n = -[(K + s_0 N)^{-1} N]^T w_n$$

Tridiagonal matrix

$$T_q = \begin{bmatrix} \alpha_1 & \beta_2 & 0 & \dots & 0 \\ \rho_2 & \alpha_2 & \beta_3 & \dots & \vdots \\ 0 & \rho_3 & \dots & \dots & 0 \\ \vdots & \vdots & \vdots & \vdots & \beta_q \\ 0 & \dots & 0 & \rho_q & \alpha_q \end{bmatrix}$$

References

- [1] B. Gustavsen, A. Semlyen, "Rational Approximation of Frequency Domain Responses by Vector Fitting," *IEEE Trans. Power Delivery*, vol. 14, no. 3, pp. 1052-1061, 1999.
- [2] P. Feldmann and R. A. Freund, "Efficient Linear Circuit Analysis by Padé Approximation via the Lanczos Process," *IEEE Trans. Computer-Aided Design*, vol. 14, no. 5, pp. 639-649, May 1995.

Generation of Equivalent Circuit

Reduced admittance function

$$Y(s_0 + \sigma) = k_\infty + \sum_{j=1}^q \frac{k_j}{\sigma - p_j}$$

$$= \frac{1}{Z_\infty} + \frac{1}{Z_1} + \frac{1}{Z_2} + \dots + \frac{1}{Z_q}$$

when $\sigma = 2\pi f_{\max} + j\omega$

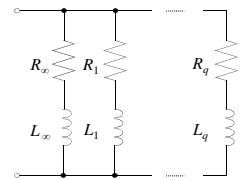
$$Z_j = \frac{-2\pi f_{\max} + j\omega - p_j}{k_j} = R_j + j\omega L_j$$

under the condition of $|p_j| > 2\pi f_{\max}$

$$k_j = \frac{-I' r \mu_j v_j}{\lambda_j}, p_j = \frac{1}{\lambda_j}$$

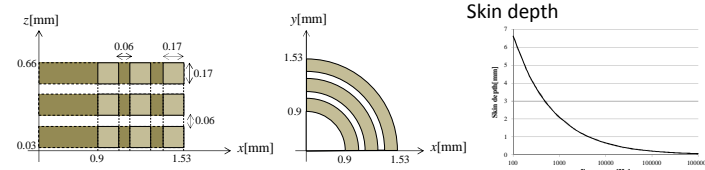
$$k_\infty = \sum_{j=0}^q I' r \mu_j v_j$$

Foster Circuit



Numerical Results

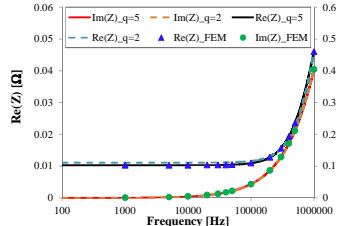
Coil windings model



Analysis condition

Conductivity κ [S/m]	Relatively Permeability μ_r	Maximum frequency f_{\max} [MHz]	Number of elements (tetrahedral elements)	Number of nodes
5.76×10^7	1	1	298201	52077

Impedance with respect to frequency

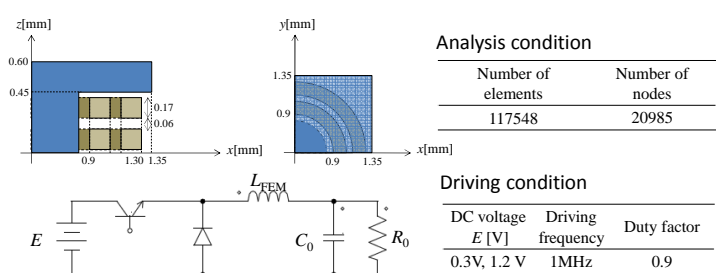


Circuit parameters

q	R_1 [Ω]	R_2 [Ω]	R_3 [Ω]	R_4 [Ω]	R_5 [Ω]
5	$1.03e-2$	5.69	6.82	141	58.3
2	$1.11e-2$	3.51	----	----	----

q	L_1 [H]	L_2 [H]	L_3 [H]	L_4 [H]	L_5 [H]
5	$6.97e-8$	$8.02e-7$	$7.25e-7$	$1.31e-6$	$1.79e-6$
2	$6.89e-8$	$3.18e-7$	----	----	----

Inductor model coupled with DC-DC converter



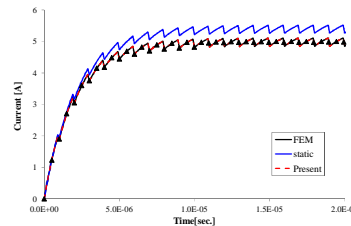
Analysis condition

Number of elements	Number of nodes
117548	20985

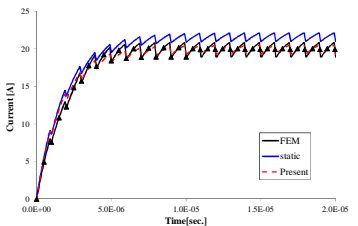
Driving condition

DC voltage E [V]	Driving frequency	Duty factor
0.3V, 1.2 V	1MHz	0.9

Currents in the case of E=0.3 V



Currents in the case of E=1.2 V



Computational time for generation of equivalent circuit

Coil Windings model	Inductor for DC-DC converter model
40 min. ($q=5$)	11 min. ($q=5$)

We use Xeon W5590/3.2GHz(12GB RAM)

Circuit analysis VS FE analysis

Coil Windings model	DC-DC converter model			
FEM*	Present	FEM(E=0.3V)	FEM(E=1.2V)	Present
230 min.	less than 1 sec.	240 min.	360 min.	less than 1 sec.

*the elapsed time of field computations by FEM at 13 sampling frequencies